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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/711,431	09/17/2004	Wai Ling Chung-Maloney	BUR920040128US1	5430	
46170	7590 07/07/2006		EXAMINER		
	CURTIS & CHRISTOFI ET HILLS ROAD, SUITE 3-	DO, THUAN V			
RESTON, VA	•	ART UNIT	PAPER NUMBER		
			2825	2825	
			DATE MAIL ED: 07/07/2006		

Please find below and/or attached an Office communication concerning this application or proceeding.

			lication No.	Applicant(s)	Applicant(s)	
		10/7	711,431	CHUNG-MALON	CHUNG-MALONEY ET AL.	
Office Action Summary			miner	Art Unit		
			an Do	2825	<u> </u>	
Period fo	The MAILING DATE of this commun r Reply	ication appears (on the cover sheet w	ith the correspondence a	ddress	
A SHO WHIC - Exter after - If NO - Failur Any r	ORTENED STATUTORY PERIOD F CHEVER IS LONGER, FROM THE M Issions of time may be available under the provisions SIX (6) MONTHS from the mailing date of this comm period for reply is specified above, the maximum stre to reply within the set or extended period for reply eply received by the Office later than three months a and patent term adjustment. See 37 CFR 1.704(b).	AILING DATE Of 37 CFR 1.136(a). In nunication. atutory period will apply will, by statute, cause	OF THIS COMMUNION no event, however, may a rand will expire SIX (6) MOI the application to become A	CATION. reply be timely filed NTHS from the mailing date of this BANDONED (35 U.S.C. § 133).		
Status						
2a) <u>□</u> 3) <u>□</u>	Responsive to communication(s) file This action is FINAL . Since this application is in condition closed in accordance with the practi	2b)⊠ This actio for allowance ex	n is non-final. cept for formal mat	•	ne merits is	
Dispositi	on of Claims					
5)□ 6)⊠ 7)□ 8)□ Applicati 9)□ 10)□	Claim(s) 6-14 is/are pending in the at 4a) Of the above claim(s) is/a Claim(s) is/are allowed. Claim(s) 6-14 is/are rejected. Claim(s) is/are objected to. Claim(s) are subject to restrict on Papers The specification is objected to by the The drawing(s) filed on is/are: Applicant may not request that any objected to a separate that any objected that any	e Examiner. a) accepted ction to the drawir the correction is	tion requirement. or b)⊡ objected to ng(s) be held in abeya required if the drawing	nce. See 37 CFR 1.85(a). g(s) is objected to. See 37 C		
•	The oath or declaration is objected to	by the Examine	er. Note the attache	d Office Action or form P	1O-152.	
12)[/ a)[Acknowledgment is made of a claim All b) Some * c) None of: 1. Certified copies of the priority 2. Certified copies of the priority 3. Copies of the certified copies application from the Internationee the attached detailed Office action	documents have documents have of the priority do nal Bureau (PC	e been received. e been received in A cuments have beer T Rule 17.2(a)).	Application No n received in this Nationa	al Stage	
2) D Notice 3) D Inform	e(s) e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (Penation Disclosure Statement(s) (PTO-1449 or No(s)/Mail Date		Paper No	Summary (PTO-413) (s)/Mail Date Informal Patent Application (PT 	ГО-152)	

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DETAILED ACTION

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1. This office action is responsive to elected restriction dated on 06/09/2006. Claims 6-14 are pending in this office action. Claims 1-5 are the non-elected ones and requested to be cancel in the next response.

Applicant's representative elected group 2, claims 6-14 with traverse. Since the application contains claims directed to the following patentably distinct species of the claimed invention:

Group I, claims 1-5 draw to selecting a smallest chip image from among a plurality of chip images that can supply at least a required number of I/O cells; determining a number of excess I/O kernels of said smallest chip image in excess of said required number of I/O cells; computing a number of other cells types which can be provided by the core area of the image and the area of the excess I/O kernels; and evaluating from a result of said computing step if a required number of cells for said integrated circuit design can be provided within said smallest chip image if said excess I/O cells or I/O kernels are depopulated from said smallest chip image.

Group II, claims 6-14 draw to an array of I/O kernels, each I/O kernel having a plurality of contiguous I/O cells having common power connections, independent of any other I/O kernel; and a plurality of contiguous depopulated I/O cell sites corresponding to an area of a said I/O kernel and having core cells formed therein.

The distinction between two groups is the search of group II that does not require the different elements of group I such as selecting a smallest chip image, determining a number of excess I/O kernels, computing a number of other cells types and evaluating from a result. Therefore, these invention are distinct and the restriction for examination purposes as indicated is proper.

Applicant is reminded that upon the cancellation of claims to a non-elected invention, the inventorship must be amended in compliance with 37 CFR 1.48 (b) if one

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or more of the currently named inventors is no longer an inventor of at least one claim remaining in the application. Any amendment of inventorship must be accompanied by a diligently-filed petition under 37 CFR 1.48(b) and by the fee required under 37 CFR 1.17(h).

Claim objections

Claim 6, the term "independent of any other I/O kernel ";

Claim 14, the term "I/O cell sites associated with a contiguous plurality of other of said connection pads" are unclear to what applicants intend to mean.

Clarification or correction is required.

Claim Rejections - 35 USC 112

2. Claim 6 is rejected under 35 U.S.C. 112, first paragraph, because the specification, while being enabling for "independent" function, does not reasonably provide enablement for "independent of any other I/O kernel". The specification does not enable any person skilled in the art to which it pertains. Examiner does not know how to make the "independent" function to the other I/O cells.

35 U.S.C. 101 reads as follows:

Whoever invents or discovers any new and useful process, machine, manufacture, or composition of matter, or any new and useful improvement thereof, may obtain a patent therefor, subject to the conditions and requirements of this title.

3. Claims 6-11,13,14 are rejected under 35 U.S.C. 101 and unpatentable because the claimed invention is directed to non-statutory subject matter. The claimed invention lacks patentable utility.

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The claims appear to use abstract functions or definitions without providing a useful, concrete and tangible result.

The clarification of claim descriptions for the patentable utilities is required.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

- (e) the invention was described in-
- (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effect under this subsection of a national application published under section 122(b) only if the international application designating the United States was published under Article 21(2)(a) of such treaty in the English language; or
- (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that a patent shall not be deemed filed in the United States for the purposes of this subsection based on the filing of an international application filed under the treaty defined in section 351(a).
- 4. Claims 6-14 are rejected under 35 U.S.C. 102(e) as being unpatentable over the prior art of Ali et al., Pat. No. 6836026.

Regarding claim 1: The prior art teaches a circuit comprising:

an array of I/O kernels, each I/O kernel having a plurality of contiguous I/O cells having common power connections, independent of any other I/O kernel (col. 7, lines 38-58); and

a plurality of contiguous depopulated I/O cell sites corresponding to an area of a said I/O kernel and having core cells formed therein (col. 3, lines 21-35 and col. 9, lines 25-38 using cell replacement).

Regarding claims 7,8: The prior art teaches a circuit with connection pads (col. 9, lines 25-38).

The remaining claims of 102(e) section contain features similar to the rejection of above claims and rejected in the rationale.

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CONTACT INFORMATION

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thuan Do whose telephone number is 571-272-1891. The examiner can normally be reached on Monday-Friday 8:30-4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jack Chiang can be reached on 571-272-7483. The fax phone numbers for proceeding this application is 571 273-8300.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 800-786-9199.

Thuan Do

Primary examiner 06/30/2006

Mundo